

IN THE ABSTRACT OF THE DISCLOSURE:

Please amend the paragraph at page 20, lines 3-13, as follows:

-- An internal voltage generator of a semiconductor device features a tuning unit, a characteristic controller and an internal voltage ~~generator~~ generating unit. The tuning unit receives a test mode signal, an external signal and a signal stored in an internal setup device, and outputs a control signal. The characteristic controller receives the control signal, and outputs a characteristic controlling signal. The internal voltage ~~generator~~ generating unit receives a reference input signal and the characteristic controlling signal, and controls a characteristic of an internal voltage.--.